

University of Mumbai

**1T04614**

Winter 2025

EXAMINATION TIME TABLE

PROGRAMME - S.E. (VLSI Design & Technology) (Choice Based) (R-2022-23 'C' Scheme)**SEMESTER - IV**

Days and Dates	Time	Paper Code	Paper
Tuesday, 2 December, 2025	02:30 p.m. to 05:30 p.m.	31141	Engineering Mathematics-IV
Thursday, 4 December, 2025	02:30 p.m. to 05:30 p.m.	31142	Electronic Devices & Circuits II
Monday, 8 December, 2025	02:30 p.m. to 05:30 p.m.	31143	Microcontrollers Applications
Wednesday, 10 December, 2025	02:30 p.m. to 05:30 p.m.	31144	Principles of Communication Engineering
Friday, 12 December, 2025	02:30 p.m. to 05:30 p.m.	31145	Signals & Systems

Important Note: •The candidates appearing for the examination should report 20 minutes before the start of examination.

- Mobile phones and other electronic gadgets are prohibited in the examination hall.
- Change if any, in the timetable shall be communicated on the University website.

Mumbai - 400 098
20th October, 2025.

Dr. Pooja Raundale
Director
Board of Examinations & Evaluation